

Fig. 1

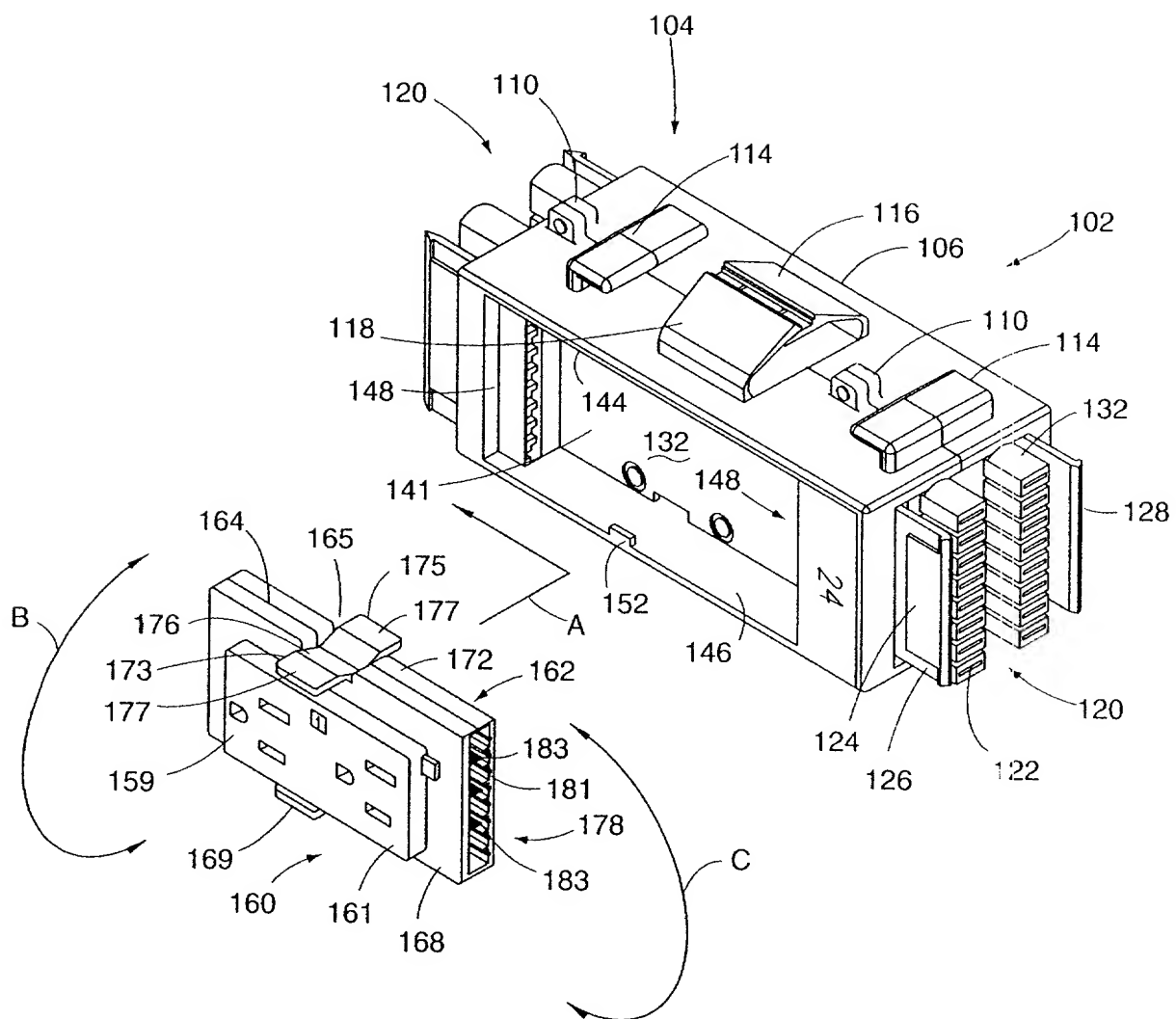


Fig. 2

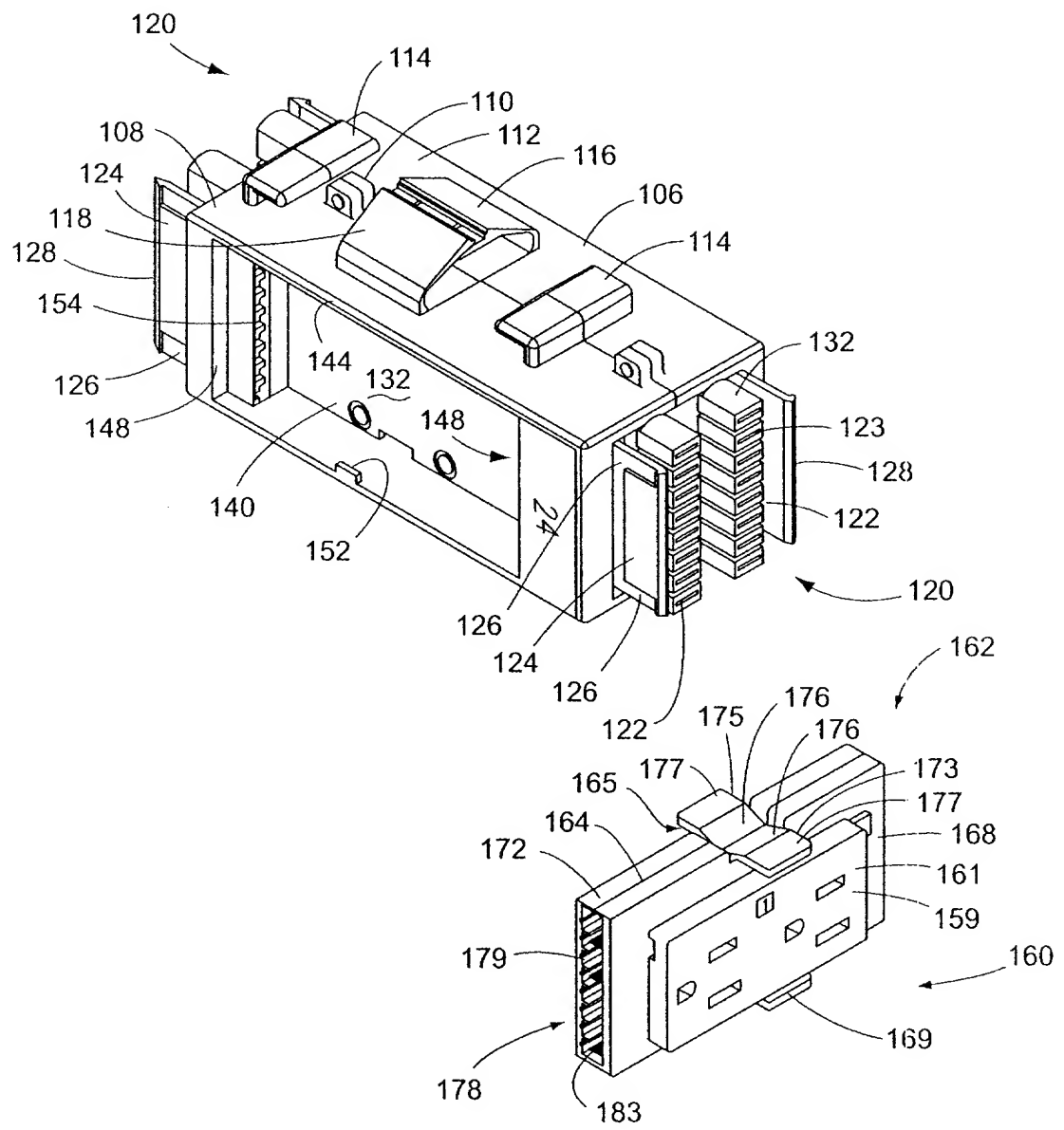


Fig. 3

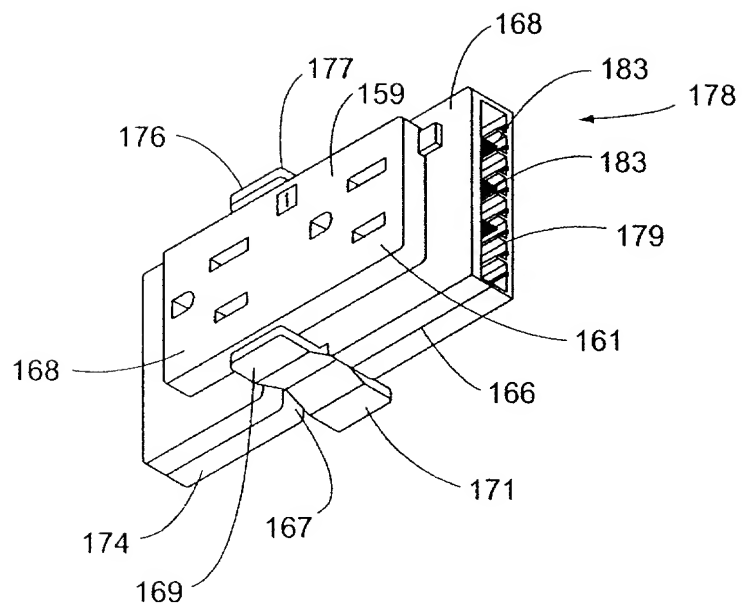


Fig. 4

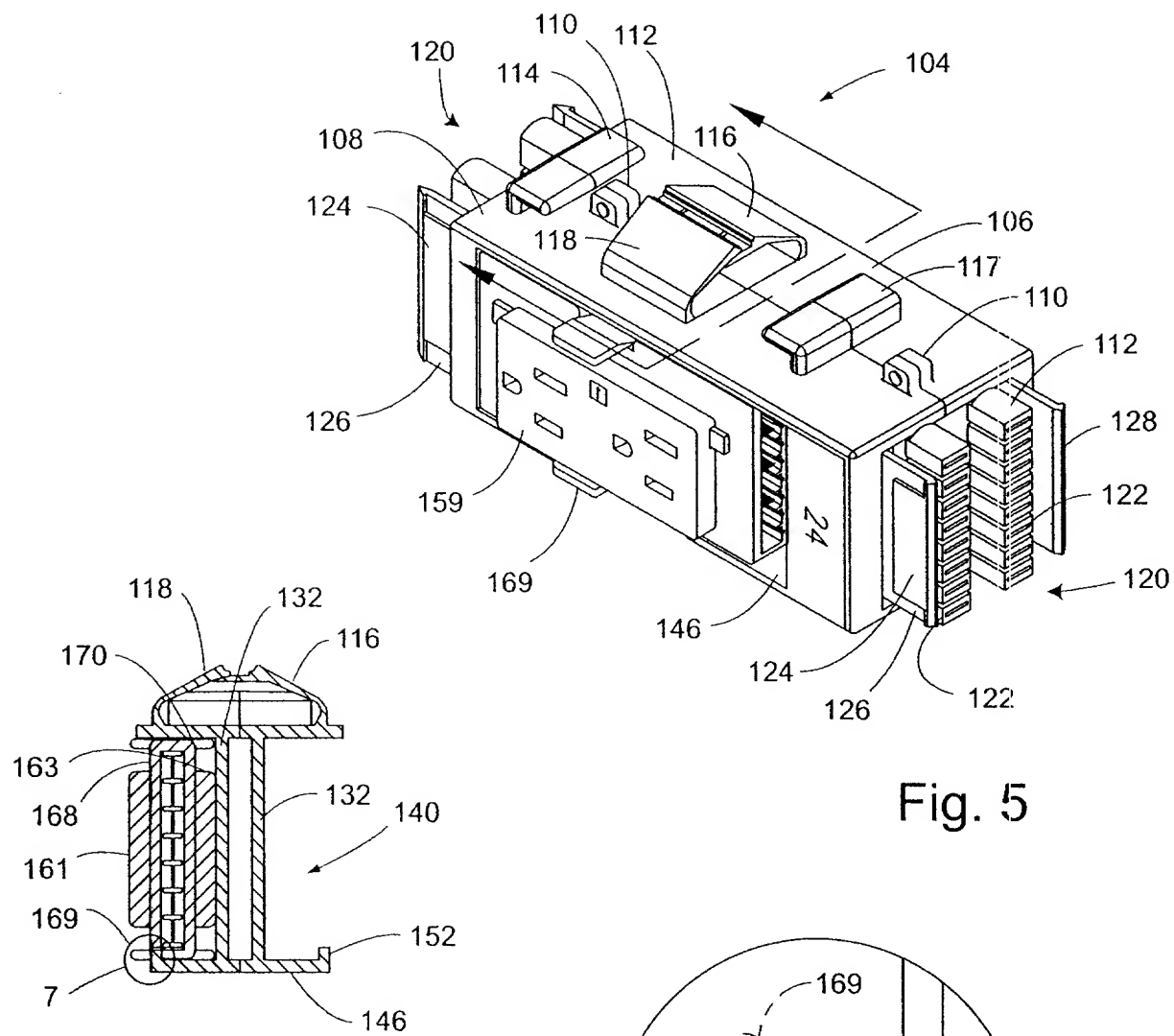


Fig. 5

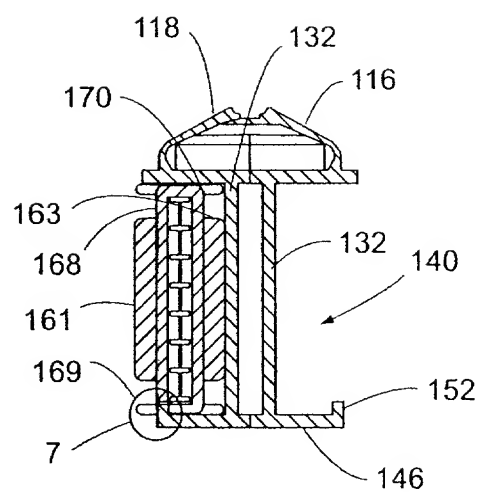


Fig. 6

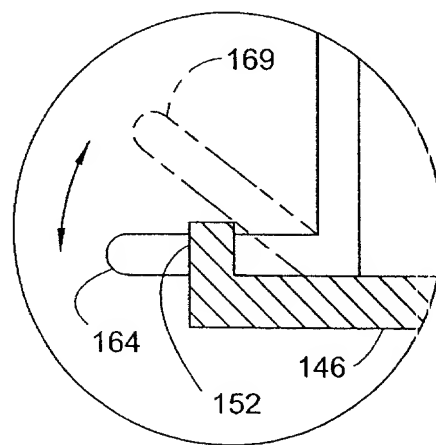


Fig. 7

Fig. 8

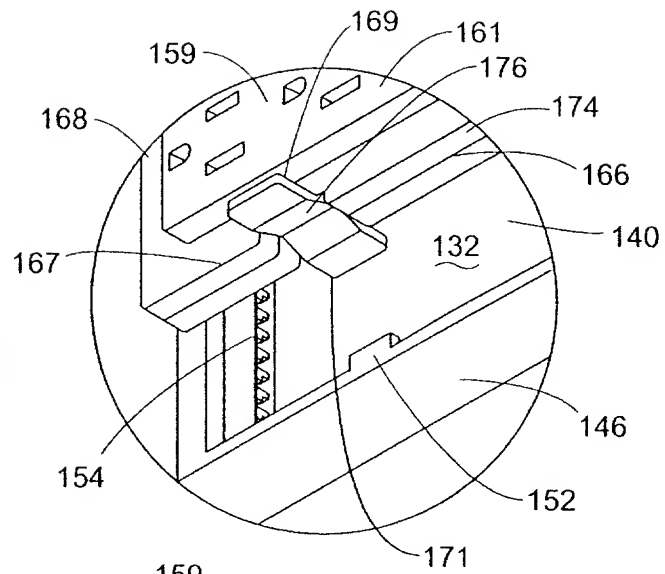


Fig. 9

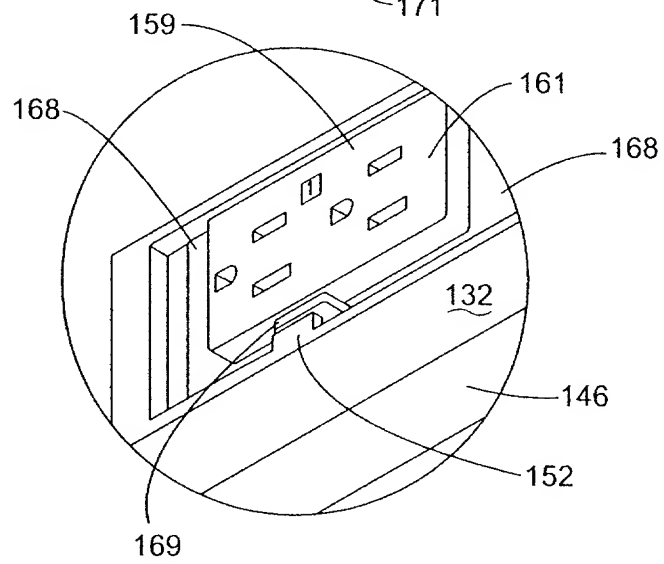
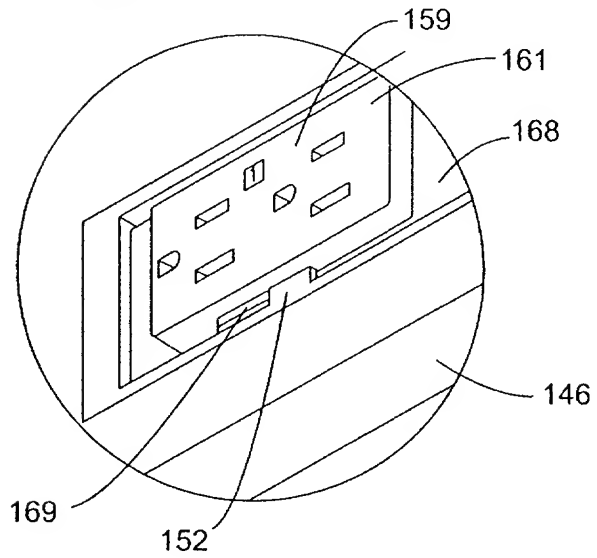


Fig. 10



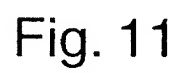


Fig. 11

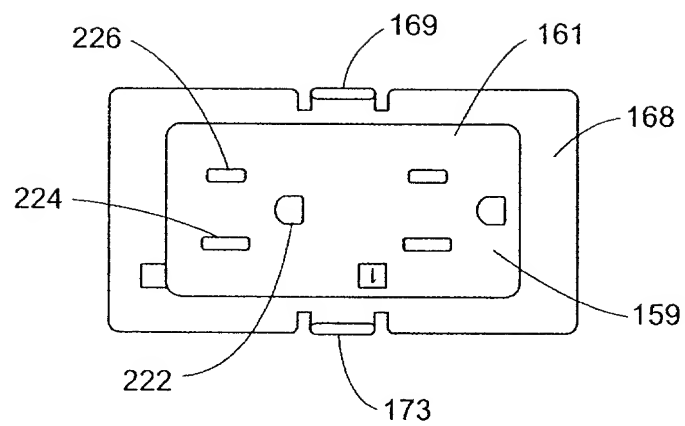


Fig. 12

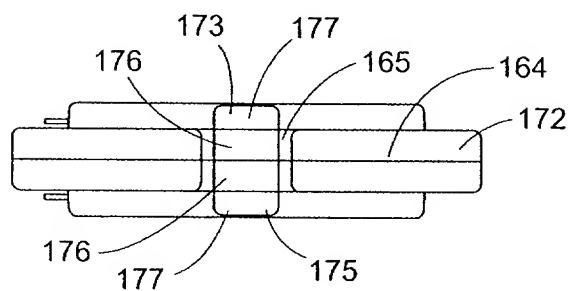


Fig. 13

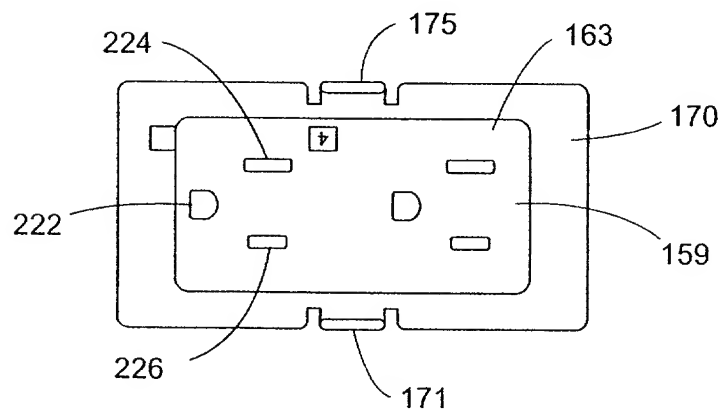


Fig. 14



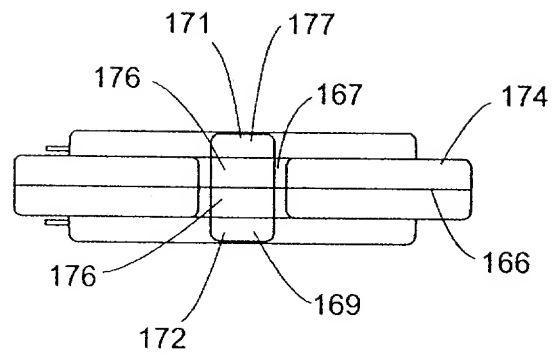


Fig. 15

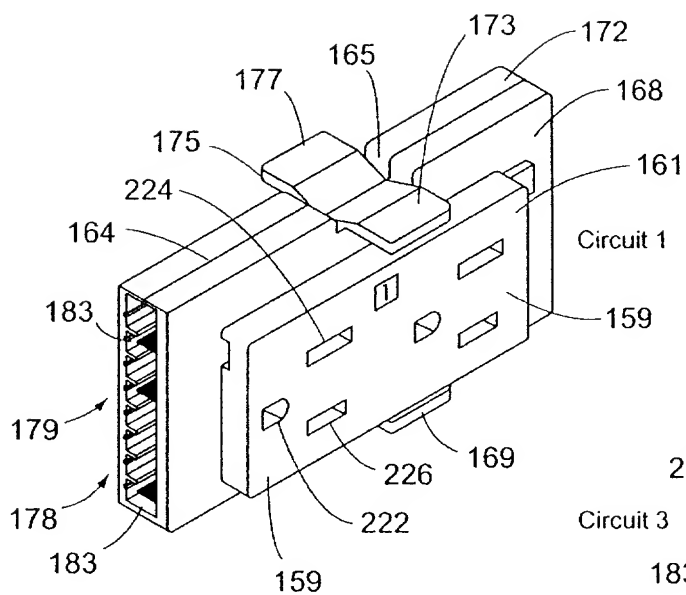


Fig. 16

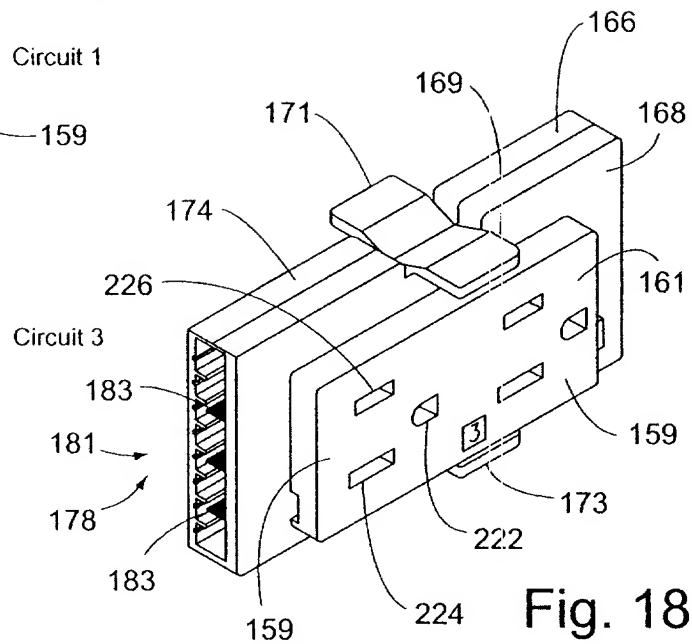


Fig. 18

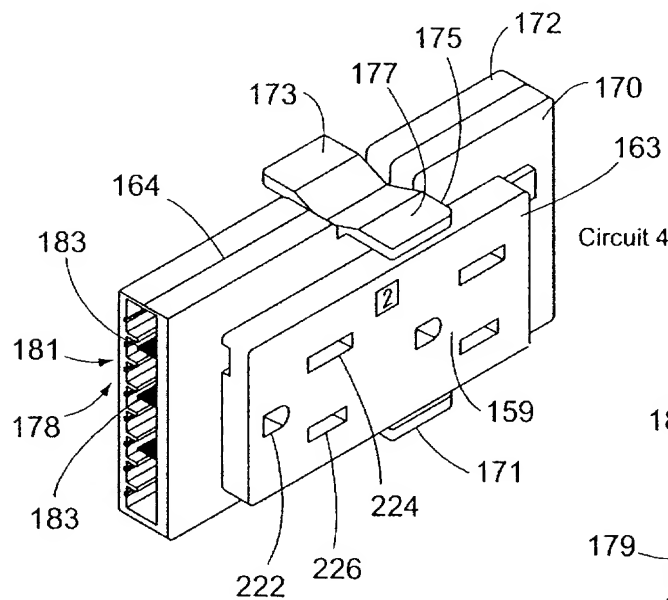


Fig. 17

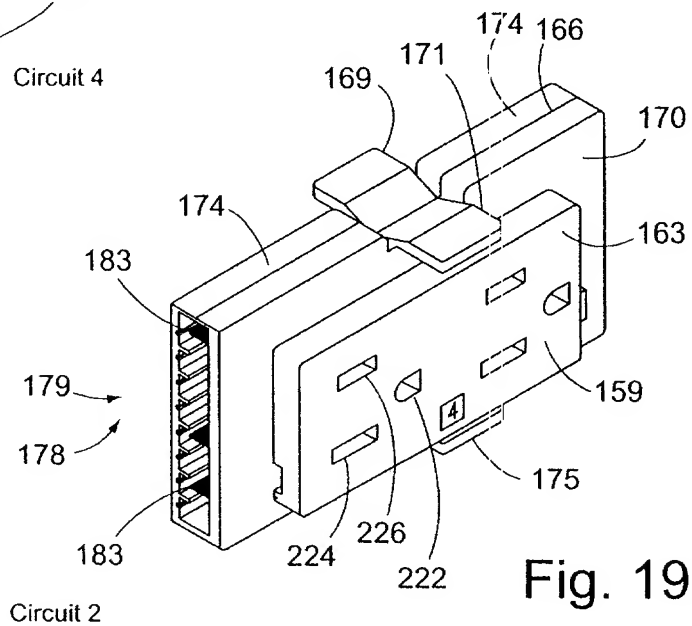
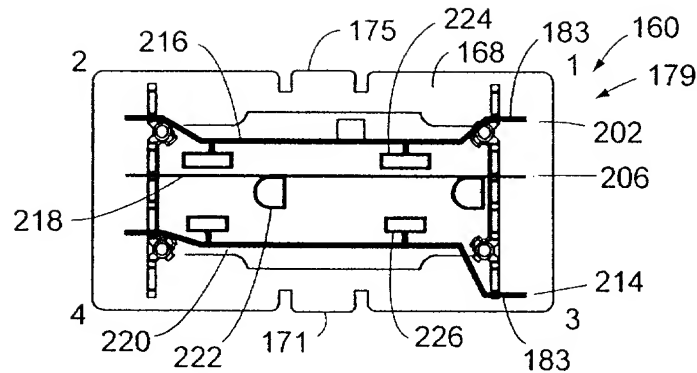


Fig. 19

Fig. 20



H-4 Circuit 1

N-1

H-3

G

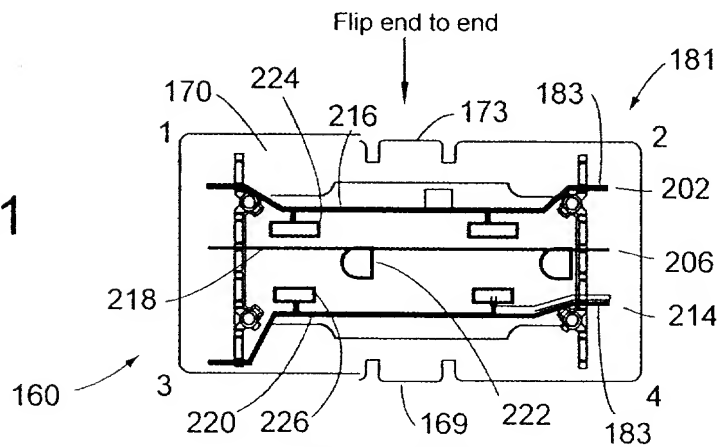
IG

H-2

N-2

H-1

Fig. 21



H-4 Circuit 2

N-1

H-3

G

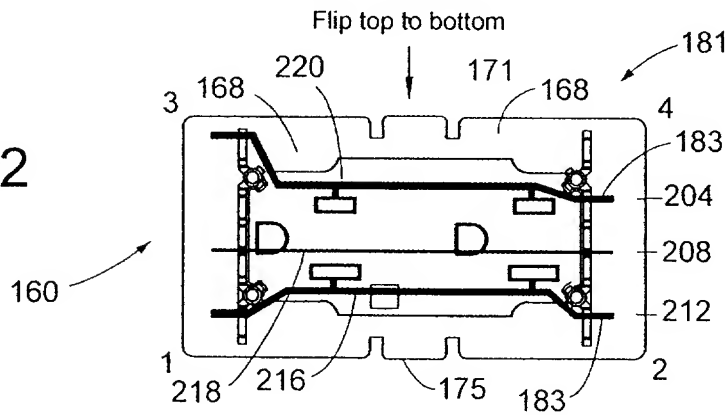
IG

H-2

N-2

H-1

Fig. 22



H-4 Circuit 3

N-1

H-3

G

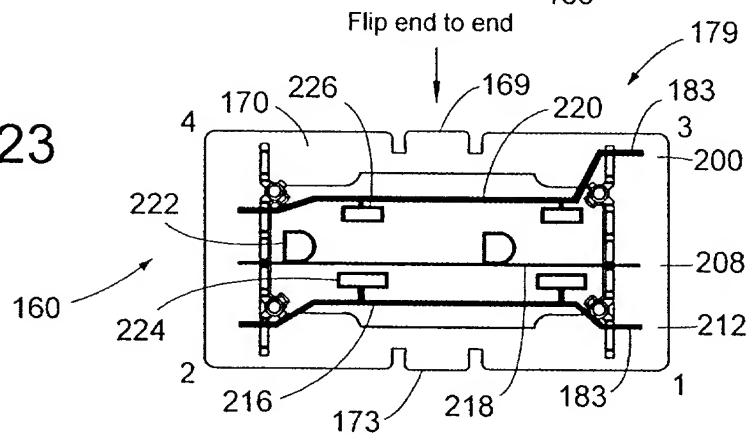
IG

H-2

N-2

H-1

Fig. 23



H-4 Circuit 4

N-1

H-3

G

IG

H-2

N-2

H-1

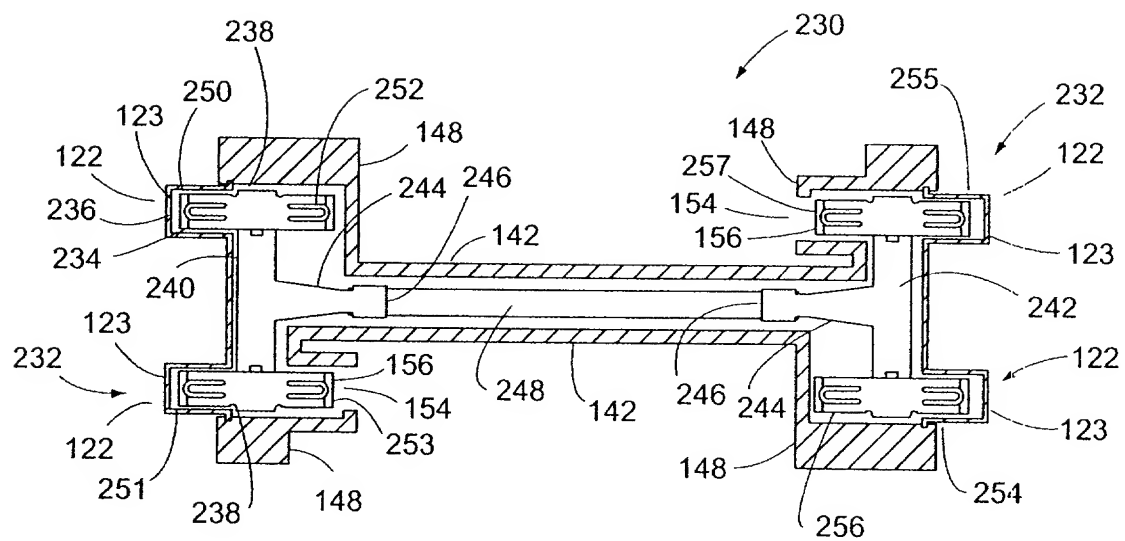


Fig. 24

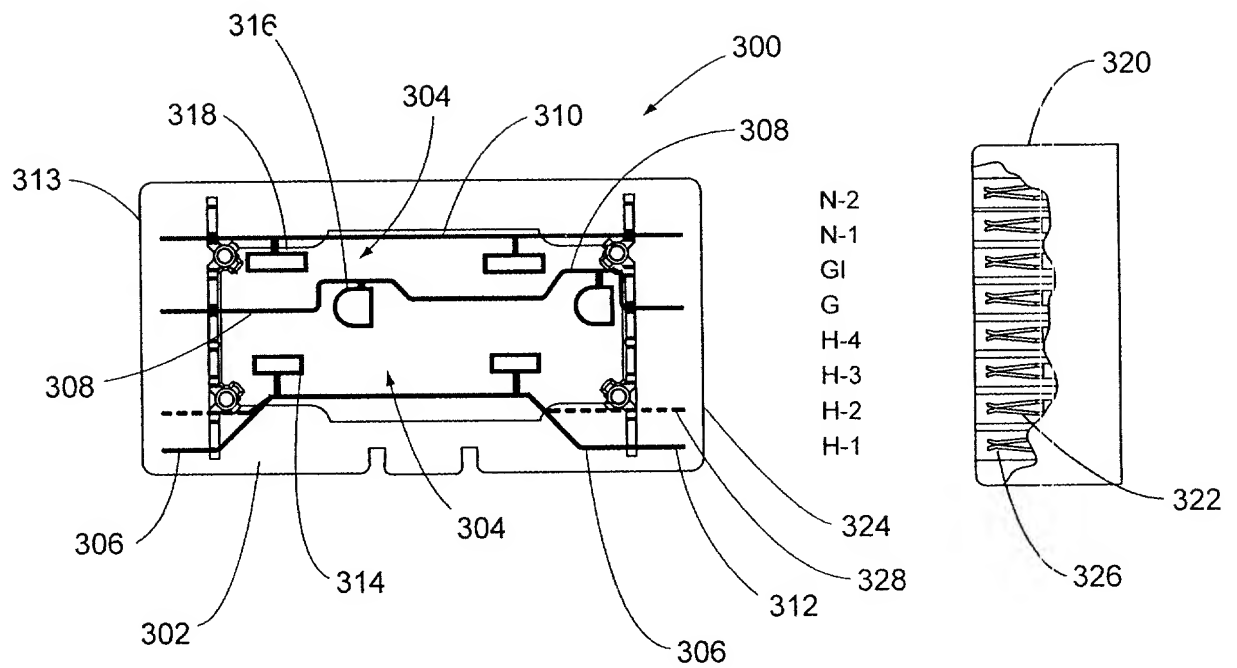


Fig. 25 (Prior Art)